

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Seok-Jun WON

Serial No.: 10/676,865 Examiner: Owens, Douglas W.

Filed: September 30, 2003 Group Art Unit: 2811

Confirmation No.: 7630

For: FLAT-TYPE CAPACITOR FOR INTEGRATED CIRCUIT AND  
METHOD OF MANUFACTURING THE SAME

Date: December 8, 2006

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR  
ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance, the Examiner recited portions of the allowed independent claims and stated that the prior art failed to teach the recited portions. The applications note that it is a well-known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

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Respectfully submitted,  
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